Requirements and novel devices for optical interconnects David Miller

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Acknowledgements

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Funding

FCRP/DARPA Interconnect Focus Center

Intel

For a copy of these slides, send email to <u>dabm@ee.stanford.edu</u>

Main reference D. A. B. Miller, Proc. IEEE 97, 1166 - 1185 (2009)

DARPA EPIC, UNIC, and Optocenters programs

AFOSR Plasmonics and Robust and Complex On-Chip Nanophotonics MURIs

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Summary

Reasons why we have to use optical interconnects Energy Density

How are we going to do that?

Where are the opportunities?

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Main reference D. A. B. Miller, Proc. IEEE 97, 1166 - 1185 (2009)

Growth in information communication and processing



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Memory bandwidth and floating point performance of graphics processor chips



Of the 225 pJ required for an operation

- 50 pJ is for logic and arithmetic operations
- Most of the rest is for interconnect to move the data on and off chip
- Accessing this data from external DRAM memory takes more than 10 nJ

Current graphics processing
chip (Nvidia *Fermi*)
1.6 GHz internal clock rate
130 W
665 Gflops peak (double precision)
175 GB/s peak off-chip bandwidth
1.4 Tb/s
Note that total 2010

internet traffic ~ 60 Tb/s

S. W. Keckler, W. J. Dally, B. Khailany, M. Garland, and D. Glasco, "GPUs and the Future of Parallel Computing," IEEE Micro 31, No.5, 7 – 17 (Sept./Oct. 2011)

Projected chip performance – bytes/FLOP



Input/Output interconnect (I/O) rate does not keep up with ability of chip to calculate

Ideal of 1 Byte of memory access for each floating point operation (FLOP) cannot be retained

• Byte/FLOP gap

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Power dissipation in electrical interconnects

Dissipation in electrical interconnects is from charging and discharging wire capacitance

Wires always have large capacitance per unit length

~ 2 pF/cm

Simple logic-level signaling results in specific dissipation

E.g., at 2pF/cm and a 2 cm chip, at 1 V on-off signaling

Dissipate at least ~ $\frac{1}{2}$ CV² per bit sent across chip ~ 2pJ

electrical connection



Saving energy with optical interconnects – "quantum impedance conversion"



Optics only has to charge the photodetector and transistor to the logic voltage, not the interconnect line

Available energy for interconnects



- 50 100 fJ/bit system energies are low enough for off-chip interconnect
- Optical output devices need to have energies of ~ 10 fJ for desired I/O in later years
- Even lower energies desirable for on-chip interconnect

Approaches to reduce device energy

Integrate optics and optoelectronics compatible with silicon CMOS

Germanium quantum well modulators

Very low energy devices potentially integrable with CMOS

Exploit nanophotonics for compact very low energy devices Nanophotonic resonators Nanometallic enhanced photodetectors Potentially very low capacitance for low total energy of communication

QCSE in germanium quantum wells on Si

Quantum-confined Stark effect (QCSE) Strongest high-speed optical modulation mechanism Used today for high-speed, low power telecommunications with III-Vs

QCSE in germanium quantum wells on silicon substrates

compatible with CMOS fabrication

Can work over C-band at 1.55 µm Surprises

Works very well in "indirect gap" semiconductor

Uses Ge direct gap absorption



Y.-H. Kuo, Y.-K. Lee, Y. Ge, S. Ren, J. E. Roth, T. I. Kamins, D. A. B. Miller & J. S. Harris, Nature 437, 1334-1336 (2005)

Funded by DARPA EPIC Program, Intel, MARCO/DARPA Interconnect Focused Research Center J. Harris and D. Miller groups, Stanford University

Quantum-confined Stark effect

 $\mathbf{E} = \mathbf{0}$

E ≠ 0



Electron and hole energy levels shift with field

 Causes, e.g., reduction in lowest (1 – 1) transition energy

See

"Quantum Mechanics for Scientists and Engineers," (Cambridge, 2008)

Also upcoming

"Quantum Mechanics for Scientists and Engineers" Massive open on-line course (MOOC) See <u>http://class.stanford.edu</u> Starting September 24, 2013

Ge quantum well modulator on Si



Ge quantum well waveguide-integrated modulator

10 microns long, 0.8 microns wide, 500 nm thick intrinsic region

No resonator

Selective area growth of quantum wells in silicon on insulator (SOI) waveguides

voltage tunable device, even to 1550 nm

Capacitance ~ 3 fF

3 dB modulation with 4 V bias, 1 V swing, 1460 nm

Dynamic energy per bit ~ 0.75 fJ

Tested to 7Gb/s (equipment limited)

S. Ren et al., IEEE PTL 24, 461 – 463 (2012) D. A. B. Miller, Optics Express 20, A293-A308 (2012)



Ge quantum well reflection modulator on Si



Parameter	Value
Extinction ratio (2.5 V swing)	10.3 dB
Extinction ratio (1 V swing)	7.2 dB
Insertion loss (at 2V)	3.7dB
Resonator Q (low absorption state)	350



R. M. Audet, E. H. Edwards, K. C. Balram, S. A. Claussen, R. K. Schaevitz, E. Tasyurek, Y. Rong, E. I. Fei, T. I. Kamins, J. S. Harris, and D. A. B. Miller, "Surface-Normal Ge/SiGe Asymmetric Fabry-Perot Optical Modulators Fabricated on Silicon Substrates," J. Lightwave Technol. (to be published)

QCSE shows strong modulation even for "surface-normal" operation

- Uses standard silicon substrate Asymmetric Fabry-Perot design Shows feasibility of surfacenormal modulators compatible with silicon electronics
- Potential scalability to ~ 10 fJ/bit in micropillars

Low voltage modulator diode structure

Layer	
N-doped cap	Si _{0.13} Ge _{0.87} As 6e18cm ⁻³ 60nm
spacer	Si _{0.13} Ge _{0.87} 60nm
barriers	Si _{0.19} Ge _{0.81} 17nm
QW (x5)	100% Ge 12nm
spacer	Si _{0.16} Ge _{0.84} 40nm
P-doped Buffer	Si _{0.12} Ge _{0.88} Boron 4e19cm ⁻³ 3x80nm
Si substrate	p+ doped



Electroabsorption contrast ratio >5dB over S- and C-bands with 1V drive swing Virtual substrate (p-doped buffer) only 240 nm thick Complete diode structure 560 nm thick Low operating voltage minimizes photocurrent dissipation

.

Hybrid Nanophotonic Photodetectors

Objective

Demonstrate efficient photodetection into subwavelength structures compatible with CMOS processing techniques



K. C. Balram and D. A. B. Miller, "Self-aligned silicon fins in metallic slits as a platform for planar wavelengthselective nanoscale resonant photodetectors," Opt. Express 20, 22735-22742 (2012)

Approach

- Embed a silicon photodetector inside a nanometallic slit
- Use same metals for optical confinement and electrical connections
- Exploit resonances in the combined metal/dielectric resonance
 - enhance absorption and allow tunability of spectral response



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Resonances in silicon fin in nanometallic slit - simulations





Shine light from the top on silicon fin

- Light polarized in z direction along the slit (out of the plane of the figure)
 - 5th order dielectric lateral Fabry-Perot resonance
 - Tunable by design of slit width
 - Very strong absorption

>60% of photons incident on slit absorbed in 170 nm thick Si at 850 nm
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Resonances in germanium fin in nanometallic slit



Results

Good agreement between theory and experiment

- Measured responsivity 1.2 A/W for 925 nm wide device in 280 nm thick
 - Combination of resonance and photoconductive gain

Key features

- Allows Ge detectors beyond 1550 nm Without strain or Sn incorporation
- Could permit coarse WDM splitting using different slit widths for different bands

K. C. Balram, R. M. Audet, and D. A. B. Miller, Opt. Express **21**, 10228-10233 (2013)

Density problem in electrical interconnects



Waveguide optics - available width per waveguide

Can we get enough optical channels off the edge of the chip in fibers or waveguides?

Yes, but for waveguides packed around chip perimeter we need High clock speeds and wavelength division multiplexing (WDM) for optical fibers, or dense waveguides or

spatial multiplexing

Chip

Waveguides or fibers to and from chip edge



On-chip clock

rate, no WDM

2015 Year

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1

2005

2010

6.1 um

4.4 um

1.3 µm

2020

2025

Novel optics for very large bandwidths – multiple spatial modes?

Current interest in telecommunications for fewmode fibers, free-space quantum communications

Need "loss-less" mode splitters

But how to design?

Iterative approach - randomly add and/or subtract cylinders

Successful after ~10000 steps (48 hrs on a Pentium III)

We have no idea why it works!

Negligible intuition

Can we design arbitrary linear optics without blind global iterations?



Engineer precise mode splitting with positioning of dielectric columns

Y. Jiao et al., Optics Lett. 30, 141-143 (2005)



Single mode

outputs

How to split multiple modes efficiently?

We can combine three different overlapping modes into a multimode fiber (or separate them at the output) But this scheme has splitting loss Power also is reflected or transmitted out by the beam splitters and/or dumped at the fiber inputs



R. Ryf, C. A. Bolle, and J. von Hoyningen-Huene, ECOC 2011, paper Th.12.B.1.

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How to design an arbitrary linear optical device



How to design an arbitrary linear optical device ... and how to avoid it!



Coupling an arbitrary input beam



Suppose, for simplicity, that an arbitrary input beam can be adequately described by splitting it into 4 sections

Coupling an arbitrary input beam



Suppose, for simplicity, that

an arbitrary input beam can be adequately described by splitting it into 4 sections,

each approximately uniform in intensity and "flat" in phase.

For simplicity, neglect diffraction for the moment

assuming each of these sections will propagate as a "square" section of the beam

We are effectively "sampling" the beam in four "chunks" for the moment

Coupling an arbitrary input beam



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Self-aligning beam coupler



Self-aligning beam coupler



Adjust phase shifter in first block to minimize power in first detector Adjust reflectivity in first block to minimize power again in first detector Repeat for each block

Leaves no power in detectors, all input power in output beamDavid MillerIEEE Photonics Conference, Sept. 9, 2013

Self-aligning the beam coupler



Now all the input beam power is coupled to the output beam Regardless of the form of the input beam And without any calculation or detailed calibration of devices David Miller IEEE Photonics Conference, Sept. 9, 2013 31

Self-aligning the beam coupler

Sequential process No overall iteration Only local minimization feedback loops On one parameter at a time No multiparameter global optimization No calculations at all! We can leave this process running all the time Continually optimizing as devices drift Or Optimizing for changing inputs, e.g., Atmospheric turbulence Moving sources

DM "Self-aligning universal beam coupler," Opt. Express **21**, 6360-6370 (2013)

Mach-Zehnder self-aligning implementations



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Self-aligning multiple orthogonal beams



Once we have aligned beam 1 using detectors D11 – D13 An orthogonal input beam 2 passes through the nearly transparent detectors to the second row

Where we can self-align it using detectors D21 – D22 Separating two overlapping orthogonal beams to separate outputs

Self-aligning multiple orthogonal beams



Self-aligning 2 beam Mach-Zehnder implementation



D11, D12, D13 are mostly-transparent detectors Since alignment and re-alignment need not be performed at data-rate speeds Only need small signals from the detectors Many ways of making such mostlytransparent detectors

Extensions

- Make completely arbitrary spatial optical device [1]
- Extend to different polarizations [1]
- Find optimal orthogonal optical channels through any linear optical system or scatterer [2]
- Make a spatial add/drop multiplexer for arbitrary spatial modes [3]
- Prove [1] that any physically legal linear optical device can be in principle, designed, made and self-configured, including multiple wavelength optics time-dependent optics non-reciprocal optics

 [1] "Self-configuring universal linear optical component," Photon. Res. 1, 1-15 (2013)
 [2]"Establishing optimal wave communication channels automatically," J. Lightwave Technol. DOI: 10.1109/JLT.2013.2278809

[3] "Reconfigurable add-drop multiplexer for spatial modes," Opt. Express **21**, 20220-20229 (2013)

Conclusions

What is the big problem in information processing? How to keep scaling to keep up with demand energy and bandwidth density within machines Solutions Nanotechnology for logic and interconnect Optics for interconnect Only solution for bandwidth density and interconnect energy Need low energy optoelectronics and dense nanophotonics integrated with silicon Where are the opportunities? Ultra-low energy optoelectronics? Nanometallics for local light concentration? Novel optical design approaches? We now know how to make arbitrary linear optical devices including arbitrary mode separators without any calculations!

References and links

Home page

See <u>http://www-ee.stanford.edu/~dabm/</u> for links to all topics

- Quantum mechanics for scientists and engineers" MOOC <u>http://class.stanford.edu/</u>
- Self-configuring arbitrary optics
 See overview at

http://www-ee.stanford.edu/~dabm/Selfalign.html

"All linear optical devices are mode converters," Opt. Express **20**, 23985-23993 (2012)

"How complicated must an optical component be?" J. Opt. Soc. Am. A **30**, 238-251 (2013)

"Self-aligning universal beam coupler," Opt. Express **21**, 6360-6370 (2013)

"Self-configuring universal linear optical component," Photon. Res. **1**, 1-15 (2013)

"Establishing optimal wave communication channels automatically," J. Lightwave Technol. DOI: 10.1109/JLT.2013.2278809

"Reconfigurable add-drop multiplexer for spatial modes," Opt. Express **21**, 20220-20229 (2013)

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